

	<b>L #</b>	<b>Hits</b>	<b>S arch Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>4344</b>	<b>(361/91.1,767-771,777-779,782-785,790,791,803).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>5199</b>	<b>(257/664,691,698,700,723,724,728,786).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>1591</b>	<b>(333/33,247).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>2137</b>	<b>324/765.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>398</b>	<b>(363/144,146,147).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>1294 9</b>	<b>1 2 3 4 5</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L7</b>	<b>0</b>	<b>6 and @pd&gt;=200300220</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>8</b>	<b>L8</b>	<b>1416</b>	<b>6 and @pd&gt;=20030220</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L9</b>	<b>153</b>	<b>8 and (interposer\$1 adapter\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>107</b>	<b>9 and (pin\$1 socket\$1 connector\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L11</b>	<b>46</b>	<b>9 not 10</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>20630</b>	<b>((pga pin\$1 (pin adj grid adj array\$1) with s ck t\$1) same (parasitic d lay nois\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>2079</b>	<b>1 and socket\$1</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>381</b>	<b>2 and (direct\$2 adj connect\$3)</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>315</b>	<b>((pga pin\$1 (pin adj grid adj array\$1)) with socket\$1) same (parasitic delay\$2 noise noisy)</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>133</b>	<b>4 not delay\$2</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>929</b>	<b>((socket\$1 connector\$1 pin\$1) with (introduc\$5 caus\$3) with (noise parasitic\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L8</b>	<b>470</b>	<b>7 and (((circuit wiring) adj board\$1) pcb\$1 pwb\$1 board\$1 substrate\$1)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>8</b>	<b>L9</b>	<b>50</b>	<b>8 and (pga (pin adj grid adj array\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L7</b>	<b>919</b>	<b>6 not 5</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>350</b>	<b>((socket\$1 connector\$1) with (introduc\$5 caus\$3) with (noise parasitic\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L11</b>	<b>341</b>	<b>10 not (5 9)</b>	<b>USP AT; US-P GPU B</b>